

**AMENDMENTS TO THE CLAIMS**

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (original) A method of setting a region of interest in each of a sequence of video frames, each video frame comprising a plurality of pixel values, the method including, in respect of each video frame following a predetermined number of initial frames:

generating in respect of each video frame an instantaneous region of interest;

determining whether, and if so to what extent, to modify the region of interest set for the preceding video frame, in order to determine the region of interest for the currently considered video frame, according to a stepped procedure; and

setting the region of interest of the currently considered video frame in accordance with the result of the determination performed according to the stepped procedure; wherein

the stepped procedure includes comparing a first boundary. defining the instantaneous region of interest of the currently considered frame, or sections thereof, with a second boundary defining the region of interest set for the preceding frame, or corresponding sections thereof, and if the difference is greater than a threshold amount, modifying the second boundary, or sections thereof, in the direction towards the first boundary, or the corresponding sections thereof, by an amount which is constrained to be equal to or below a maximum step amount.

2. (original) A method as claimed in Claim 1 wherein each video frame is divided into a plurality of blocks each of which comprises one or more pixels, and wherein the set region of interest is set to correspond to a plurality of whole blocks, referred to as blocks of interest, by setting each block which falls on or inside the modified second boundary as a block of interest.

**KAMARIOTIS**  
**U.S. National Phase of PCT/GB03/02528**

3. (original) A method as claimed in claim 2 further comprising generating a corresponding mask frame in respect of each of the video frames, each mask frame comprising a plurality of block values corresponding to the blocks into which each video frame is divided, and setting each block value to either a block of interest value or another value, depending on whether the corresponding block in the corresponding video frame is a block of interest or not.

4. (currently amended) A method as claimed in ~~any one of the preceding claims~~ claim 1 wherein an instantaneous region of interest and a modified set region of interest are calculated in respect of the second and each succeeding video frame of the sequence, and wherein a region of interest for the first video frame of the sequence is set to be the entire video frame.

5. (currently amended) A method as claimed in ~~any one of the preceding claims~~ claim 1 wherein the comparison and modification of boundaries is performed on a section by section basis and wherein different maximum step amounts may be set for different sections or groups of sections and/or depending on whether the modification is away from or towards the centre of the video frame and/or depending on whether a particular condition has been met or not.

6. (currently amended) A method of transmitting a video sequence from a transmitting device to a receiving device across a limited bandwidth connection, the method comprising:

generating a mask frame in respect of each video frame in the sequence according to a method as claimed in claim 3 ~~or either one of claims 4 or 5 when dependent upon claim 3,~~

processing the video frames and the mask frames in accordance with an object based encoding procedure by which objects may be encoded, and decoded at the receiving end, separately from a background part, to generate a plurality of encoded data, and

**KAMARIOTIS**  
**U.S. National Phase of PCT/GB03/02528**

packetising and transmitting the data across the limited bandwidth connection to the receiving device.

7. (original) Apparatus for setting a region of interest in each of a sequence of video frames, each video frame comprising a plurality of pixel values, the apparatus including:

generating means for generating in respect of a video frame an instantaneous region of interest;

determining means for determining whether, and if so to what extent, to modify the region of interest set for the preceding video frame, in order to determine the region of interest for a currently considered video frame, according to a stepped procedure; and

setting means for setting the region of interest of the currently considered video frame in accordance with the result of the determination performed by the determination means according to the stepped procedure; wherein

the determining means includes comparison means for comparing a first boundary defining the instantaneous region of interest of the currently considered frame, or sections thereof, with a second boundary defining the region of interest set for the preceding frame, or corresponding sections thereof, and modification means for, if the difference is greater than a threshold amount, modifying the second boundary, or sections thereof, in the direction towards the first boundary, or the corresponding sections thereof, by an amount which is constrained to be equal to or below a maximum step amount.

8. (original) Apparatus for transmitting a video sequence across a limited bandwidth connection to a receiving device, the apparatus including:

apparatus as claimed in claim 7 for setting a region of interest in each of the video frames of the video sequence,

**KAMARIOTIS**  
**U.S. National Phase of PCT/GB03/02528**

encoding means for encoding the video sequence in accordance with an object based encoding procedure, wherein the region of interest in each frame is treated as an object and the remainder of each frame is treated as background, and

transmitting means for transmitting the encoded video sequence across the limited bandwidth connection to the receiving device.

9. (currently amended) A video-conference device including the apparatus of claim 7 ~~or claim 8~~.

10. (currently amended) A carrier medium carrying computer processor implementable instructions for ~~for~~ causing a computer processor to carry out the method of ~~any one of claims 1 to 6~~ claim 1 during implementation of the instructions.